

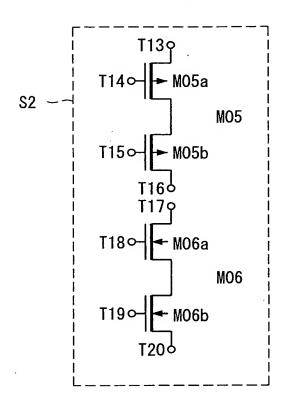
FIG. 1A

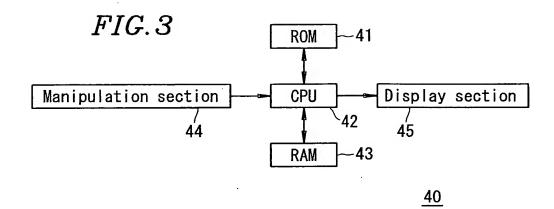
Gate electrode Gate electrode Source-drain region Source-drain region M01 i M05 M05a--M05b M02 MO3 MO6 M06a M06b M04 **S2**

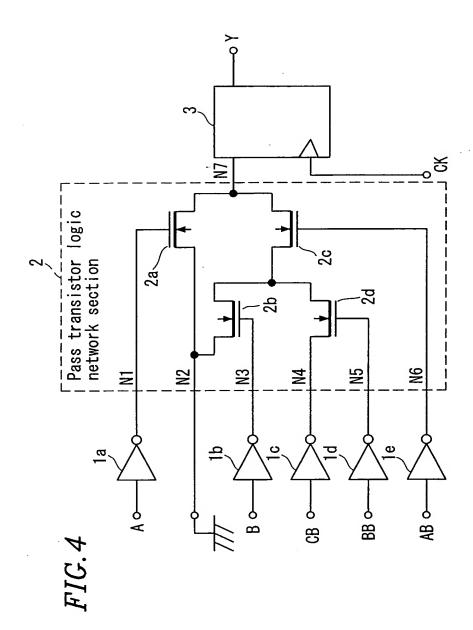
FIG. 2A

T10 T20 M01 T30 T40 T50 M02 T60 T70 T80 M03 T90 T100 T110 M04

FIG.2B







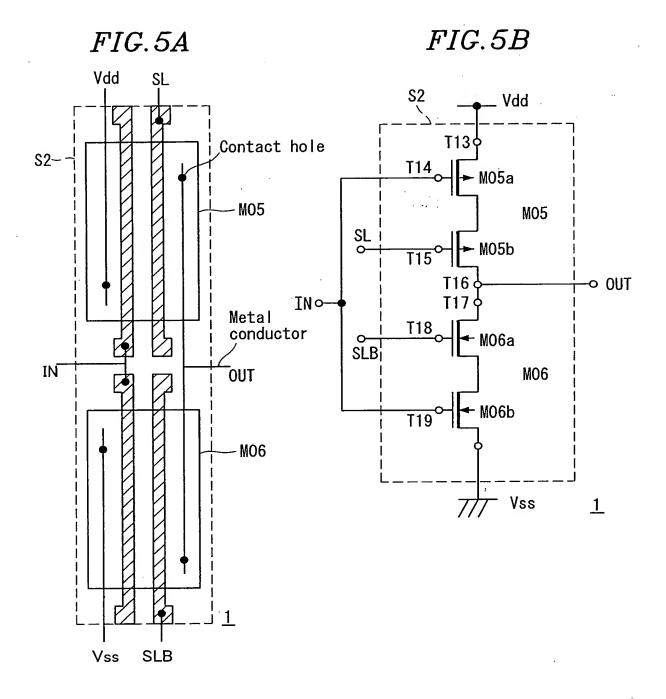
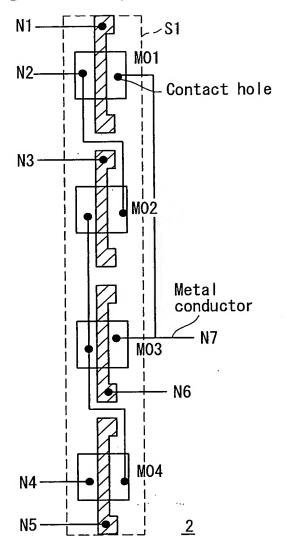
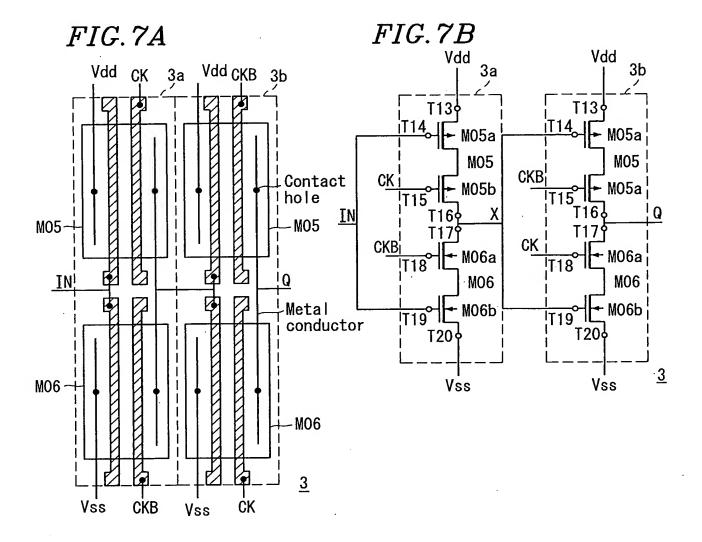
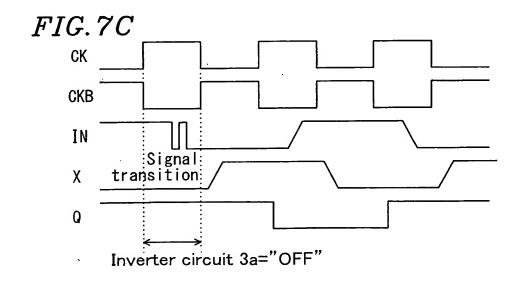
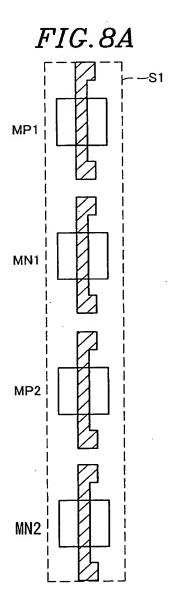


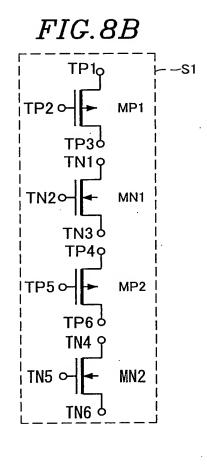
FIG. 6











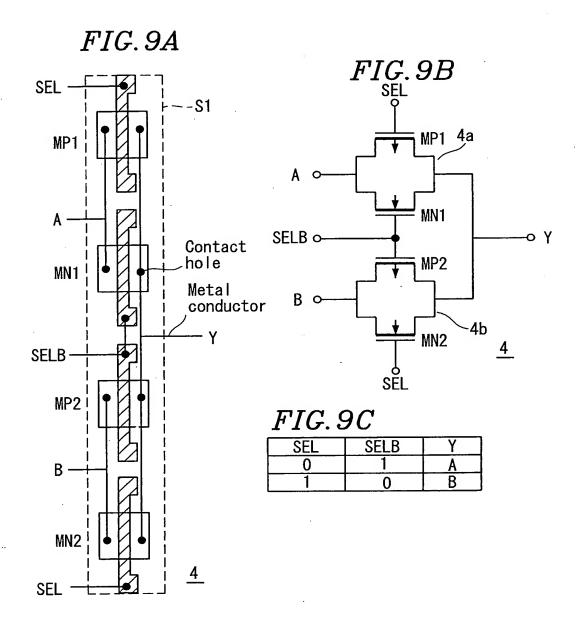
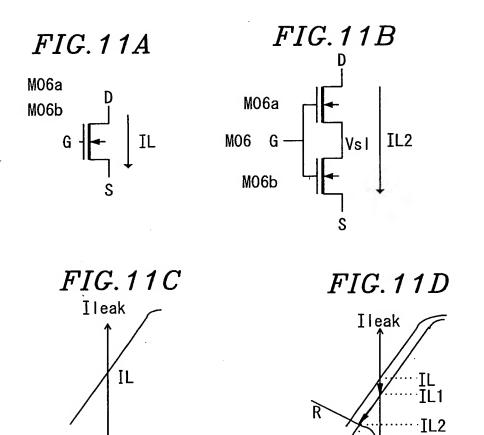


FIG. 10A Vdd SL *FIG. 10B* – Vdd Contact hole T13 c T14 - M05a M05 M05 - MO5b Metal conductor ~0UT INO IN-- OUT T18 - M06a M06 M06b SLB! T20 ¢ M06 5 Vss <u>5</u> Vss SLB

→ Vgs



→ Vgs

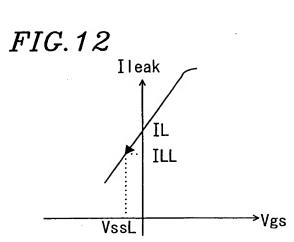


FIG. 13A

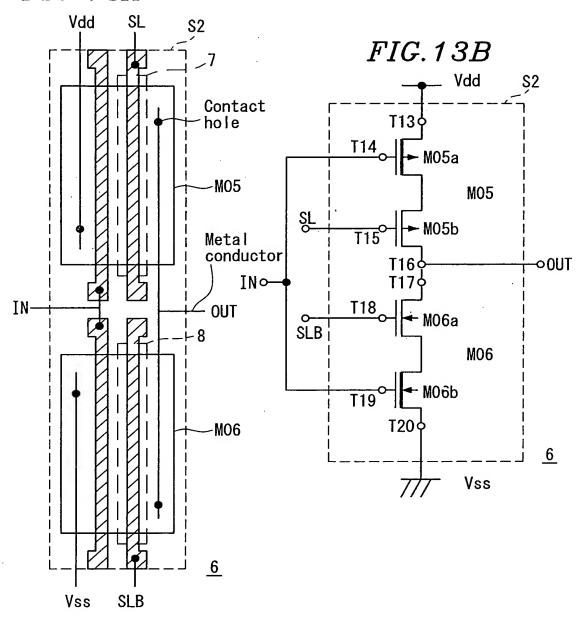


FIG. 14A

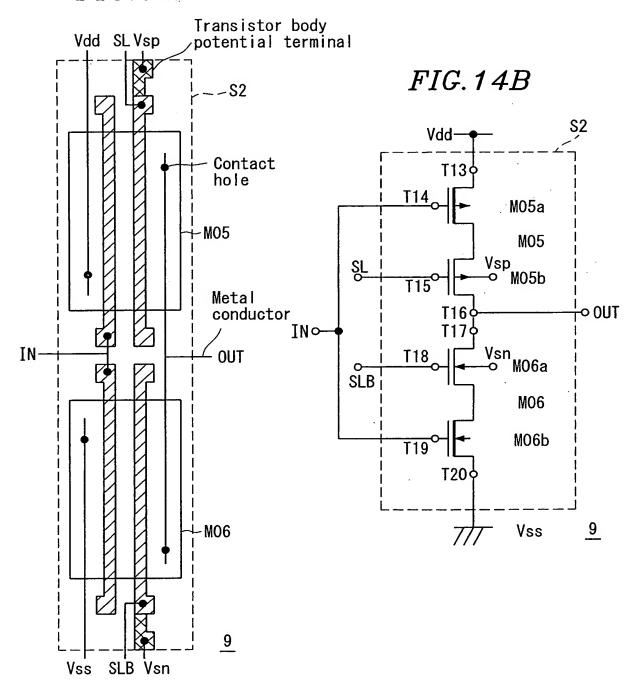


FIG. 15A

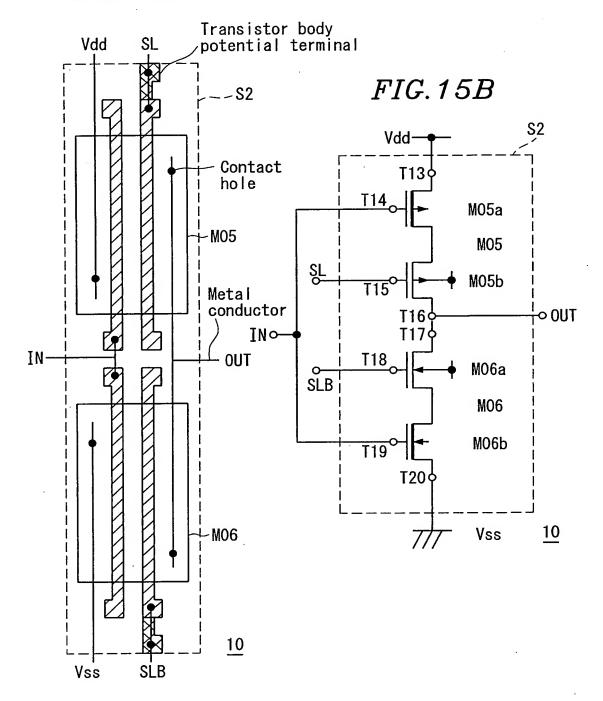
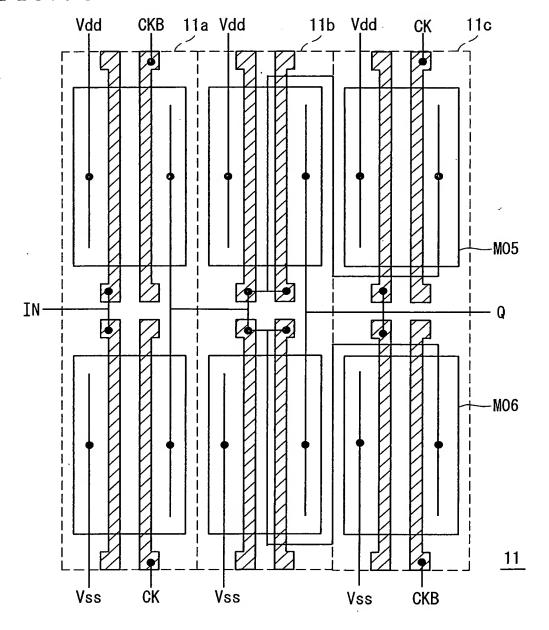


FIG. 16



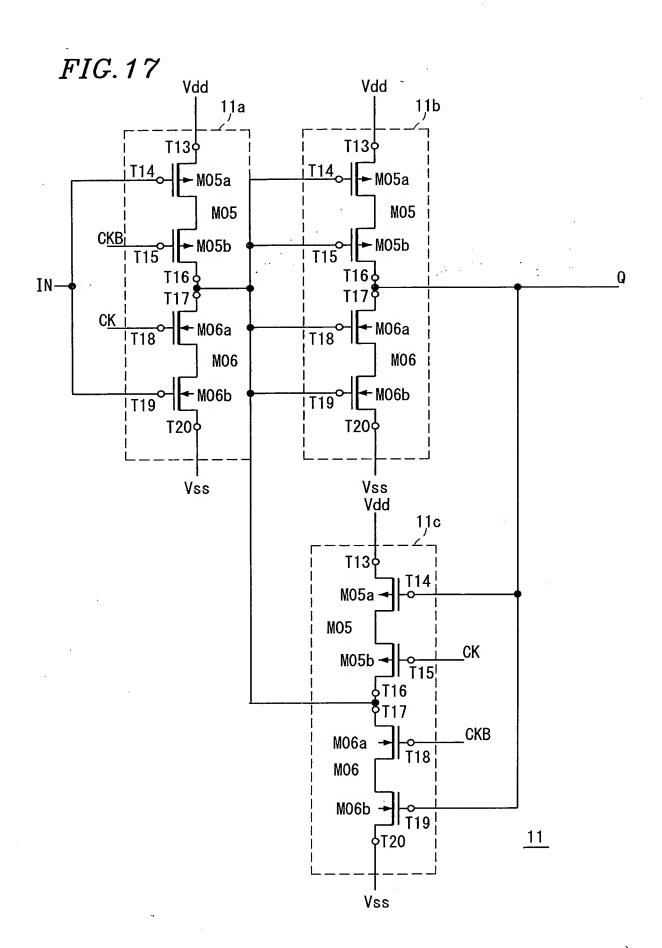


FIG. 18

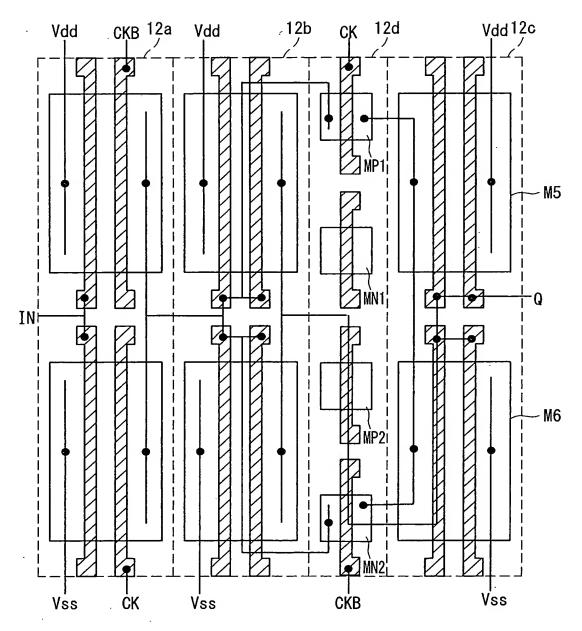


FIG. 19

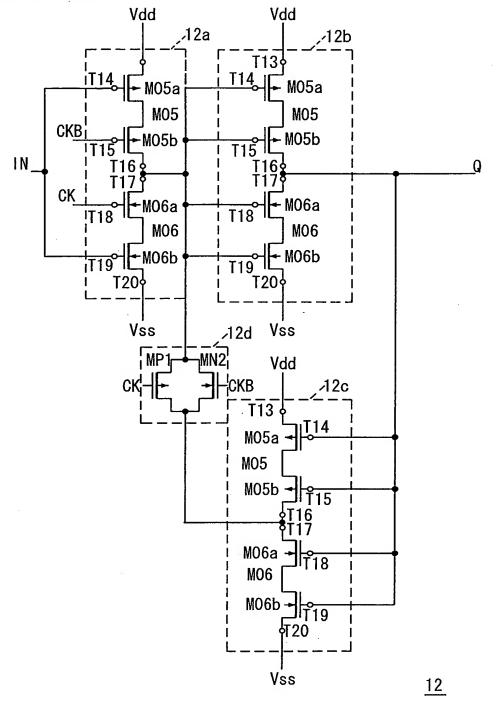


FIG.20

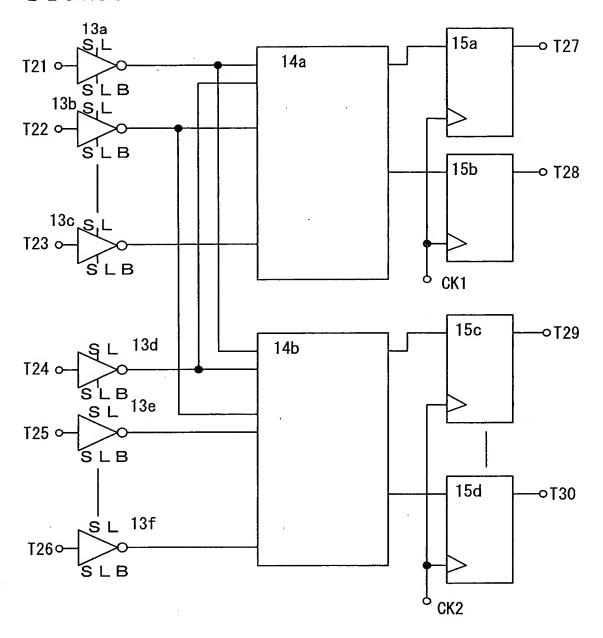
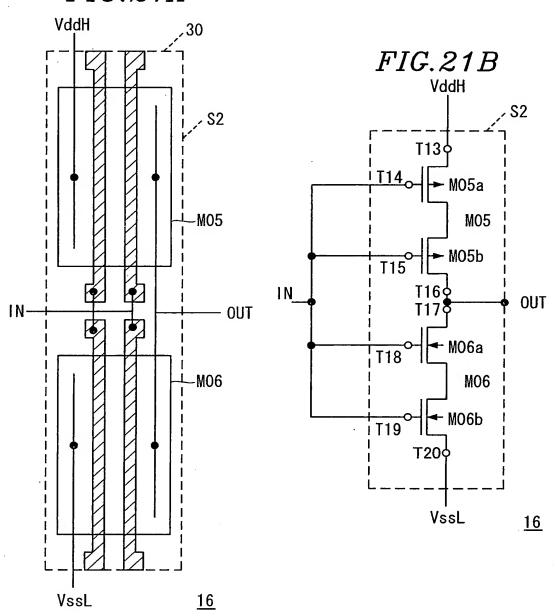


FIG. 21A



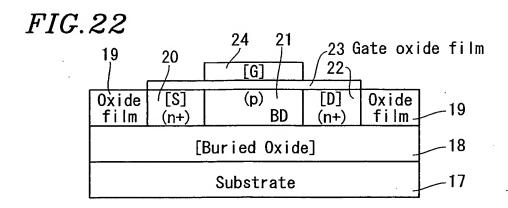
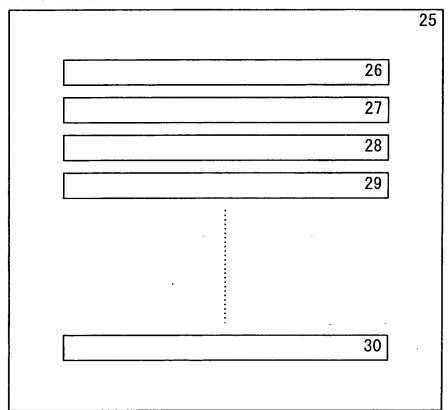
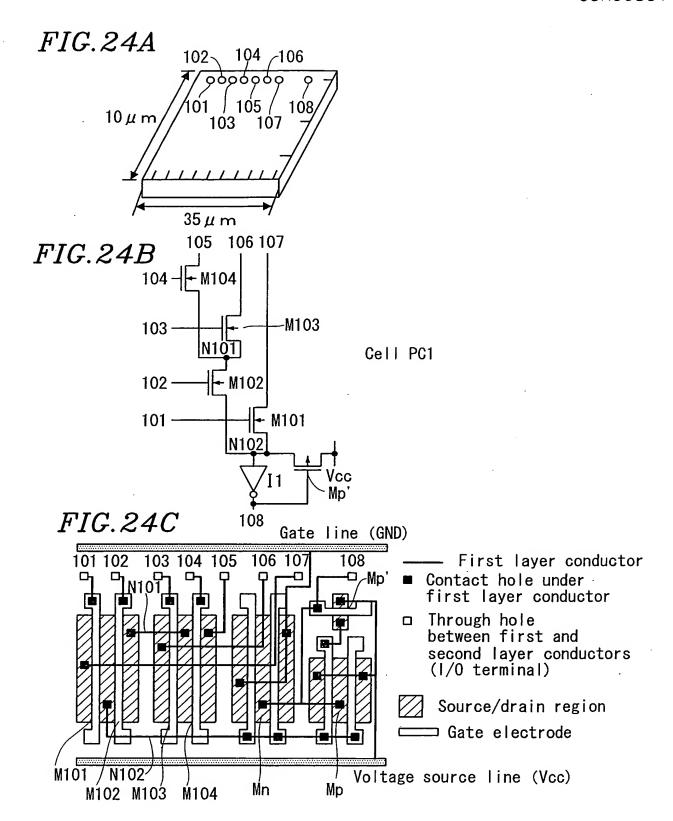


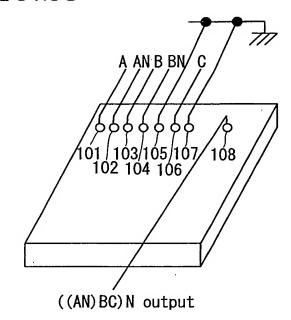
FIG.23





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FIG.25



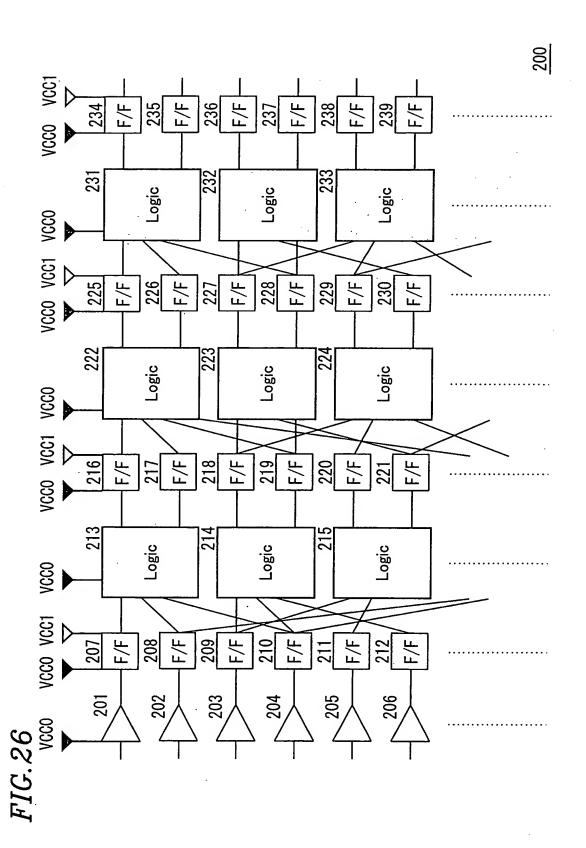


FIG.27

